VESIT

V.E.S.

Since 1962

VIVEKANAND EDUCATION SOCIETY

INSTITUTE OF TECHNOLOGY (AUTONOMOUS)

SILICON TODAY

Department of Electronics and Computer Science

JULY 25 - DECEMBER 25

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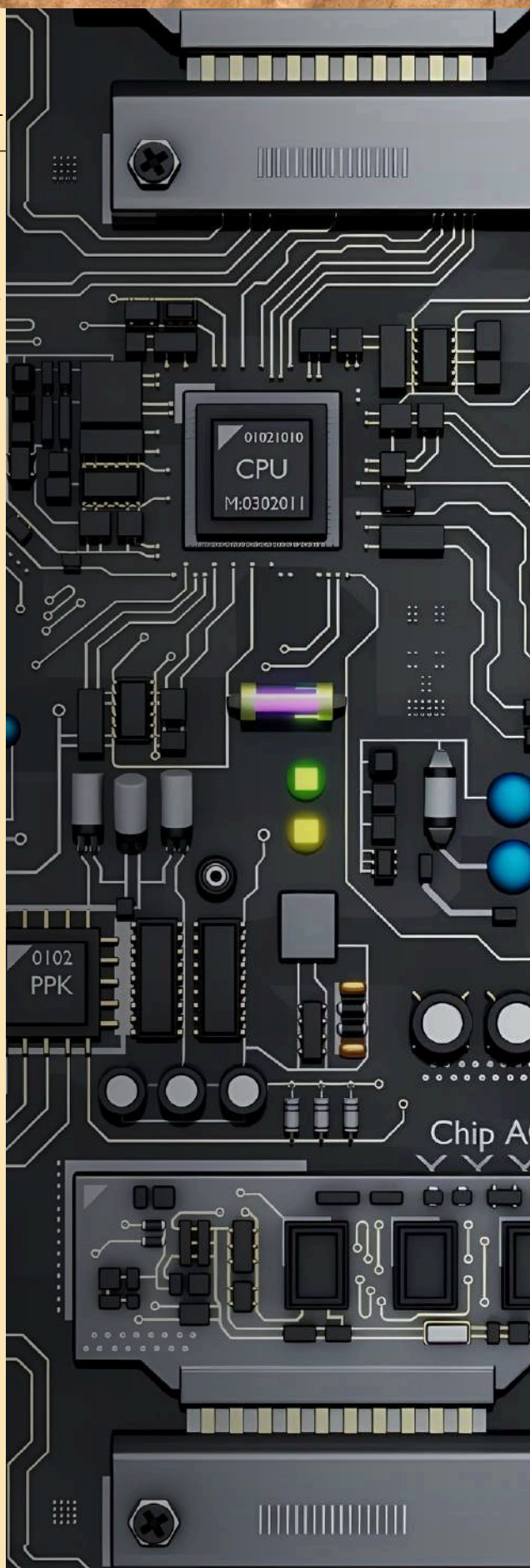


VISION AND MISSION

Vision

To produce globally competent and socially responsible engineers capable of independently solving problems with a sound knowledge of engineering in Electronics and Computer Science.

Mission

- To prepare students in the field of engineering in Electronics and Computer Science for lifelong learning.
- To develop professionals who can think critically and provide cost-effective solutions to solve local and global issues using knowledge of Electronics and Computer Science.
- To inculcate a sense of being job providers than job seekers with due consideration to ethical and social values.

MESSAGE FROM DEPARTMENT

The Integrated Engineer: Why 'Learning-It-All' is the New Mastery

Technology today is evolving rapidly, continually transforming the way we live and work. In such a changing environment, knowledge confined to a single discipline is often not enough. Many of today's innovations emerge from the intersection of different fields of knowledge.

One of the most significant examples of this convergence is the coming together of Electronics and Computer Science. Electronics provides the physical foundation through sensors, circuits, communication systems, and embedded devices, while computer science brings intelligence through algorithms, software, and data-driven decision making. Together, they power many of the technologies that define our times—from smart devices and connected systems to artificial intelligence and intelligent automation.

For students, building a sound understanding of both these domains offers a valuable advantage. It enables them to view technology as a complete system rather than as separate parts. Such interdisciplinary learning not only strengthens understanding but also prepares them better for the expectations of modern industry, where professionals who can connect hardware and software are increasingly valued.

At the same time, technology and the skills required to work with it continue to evolve. It therefore becomes important to keep developing skills, learning new tools and ideas, and staying open to change. Like the soft branch of a tree that bends with the wind yet does not break in a storm, the ability to adjust and keep learning helps one move forward even when situations change.

As technology becomes more integrated into everyday interactions, it is important to also preserve the value of direct human connection, trust, and meaningful communication.

As India moves towards the vision of Viksit Bharat 2047, the role of young engineers and technologists will be critical in building solutions that are innovative, responsible, and relevant to society.

As Satya Nadella, CEO of Microsoft, aptly said, "Don't be a know-it-all; be a learn-it-all."
Let this thought guide all learners—to remain curious, continue learning, and build the knowledge and skills needed to contribute meaningfully to the technologies of tomorrow.

Dr. T. Rajani Mangala
Professor, Department of Electronics and Computer Science

LABORATORIES AND INFRASTRUCTURE

Lab 209

Lab 206

- Laboratories are equipped with advanced instruments, including:
  - Cathode Ray Oscilloscopes (CROs)
  - Digital Storage Oscilloscopes (DSOs)
  - Function Generators
  - Signal Generators
  - Frequency Counters
  - Digital Multimeters
- Students gain practical exposure through:
  - Microprocessor trainer kits
  - Experimental trainer kits
  - Development boards
  - Interfacing boards
  - FPGA boards
- Facilities supporting circuit design and prototyping include:
  - Universal IC programmers
  - IC testers
  - PCB fabrication setups
  - SMD soldering stations
- Specialized laboratories provide exposure to industrial applications using:
  - Power Electronics and Drives kits
  - DSP kits
  - Robo Arm kits.

DEPARTMENT ACHIEVEMENTS

Faculty Contributions

Dr. Rajani Mangala
Dev-Ops Fundamental (21–25 July 2025)
Fusion 2.0 – Tech Bootcamp (21–25 July 2025)

Dr. Jaymala Adsul
Fusion 2.0 – Tech Bootcamp (21–25 July 2025)

Mr.Yogesh Pandit
Dev-Ops Fundamental (21–25 July 2025)
Fusion 2.0 – Tech Bootcamp (21–25 July 2025)

Dr. Gauri Sahoo
Fusion 2.0 – Tech Bootcamp (21–25 July 2025)

Mrs.Sarika Kuhikar
Electric Vehicle Technology (9-16 June 2025)

DEPARTMENT ACHIEVEMENTS

Faculty Interactions

Dr. Aasawri Dudwadkar

Participated in an Industry Academia Interaction organized by L&T Academy, Versova in October 2025.

Mr. Yogesh Pandit

Served as a Paper Setter for KJSIET and University of Mumbai in October 2025.

Mr.Yogesh Pandit

Served as a Paper Setter at SIES Graduate School of Technology in September 2025

Mrs. Sarika Kuhikar

Served as a Reviewer for Advanced Network Technologies & Intelligent Computing in October 2025.

Mrs. Amrita Jhaveri

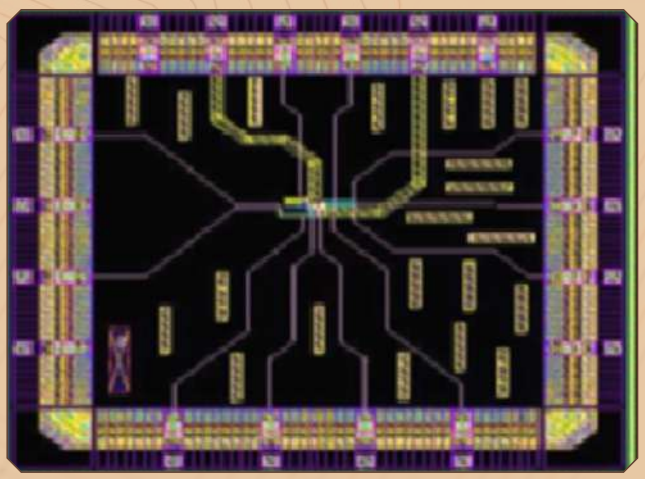
Served as a Paper Setter for University of Mumbai in October 2025.

DEPARTMENT ACHIEVEMENTS

Research Activities

The department has been actively involved under the Government of India's Chips to Startup (C2S) Programme, fostering hands-on learning and innovation in VLSI design. A notable outcome of these efforts is the development of a Programmable Gain Amplifier (PGA) IC chip, designed as part of advanced research initiatives. Fabricated at the Semiconductor Laboratory (SCL), Mohali, using 180 nm CMOS technology, the chip demonstrates strong indigenous design capability and holds applications in precision instrumentation, medical electronics, and defense systems.

VESIT in India Semiconductor Mission



GDS II of PGA

Further strengthening its research profile, the institute received a grant of ₹82.59 lakh from the Ministry of Electronics and Information Technology (MeitY) for an innovative VLSI chip design project.

DEPARTMENT ACHIEVEMENTS

Research Papers Published

Dr. Kavita Tewari

Published a research paper titled “Strain Analysis in Ni-Fe-W Alloy Using Digital Image Correlation” in an International Conference organized by Computer Vision and Robotics, held in Pune, published by Springer.

Mrs. Sarika Kuhikar

Published a research paper titled “Optimized Control Circuit Design for Single Phase Inverter with Enhanced Efficiency” in an International Conference organized by Global Knowledge Research Foundation, held in Goa, published as a Springer chapter.

Mrs. Sarika Kuhikar

Published a research paper titled “Face Recognition Based Attendance System” in an International Conference organized by Global Knowledge Research Foundation, held in Goa, published as a Springer chapter.

DEPARTMENT ACHIEVEMENTS

Research Grants

Dr. Jayamala Adsul

Received support worth 6 lakh under the Intel FPGA Academic Program, where Intel donated 2 licenses free of cost.

Received a grant of 82.59 lakhs for the project “Development of ASICs or SoCs for Programmable Gain Amplifier and Reconfigurable ADC for Wide Range of Applications” sanctioned by the Ministry of Electronics & Information Technology (MeitY) under the Chips to Startup (C2S) program.

Received funding of 9 lakh for FPGA boards and Vivado tools sanctioned by the Ministry of Electronics & Information Technology (MeitY) under the Chips to Startup (C2S) program.

ACADEMIC EXCELLENCE

Final Year Projects

The final-year students of the Department of Electronics and Computer Science demonstrated exceptional innovation and technical proficiency through a diverse range of projects developed across domains such as Artificial Intelligence, Web Development, Embedded Systems, and Internet of Things (IoT).

These projects reflected a strong integration of theoretical knowledge with practical implementation, addressing real-world challenges through creative and efficient solutions. Students showcased their ability to design, develop, and execute complete systems, incorporating modern tools, technologies, and industry-relevant methodologies.

The projects emphasized critical aspects such as problem identification, system design, performance optimization, and user-centric development. Through this process, students enhanced their technical expertise, teamwork, and project management skills.

ACADEMIC EXCELLENCE

Final Year Projects

PROJECT TITLE	PROJECT GUIDE	GROUP MEMBERS
SmartClaim: AI-Powered, Tamper-Proof & Resilient Insurance Processing System	Mr. Yogesh Pandit	Harshita Anchan, Atharva Phadtare, Mohit Kumar, Mihir Lingayat
Smart Wearable Safety System for Alzheimer's Patients (ESP32)	Dr. Asawari Dudwadkar	Ritika Zare, Anushka Shinde, Sahil Agarwal, Parnika Upkar
AGRIBro – Smart Agriculture Ecosystem	Dr. Rajani Mangala	Kalpesh Rathod, Shreyash Katole, Avani Barapatre, Sneha Bhandari, Madhav Tandon
Autonomous Intelligent System for DevOps Automation	Dr. Rajani Mangala	Binayak Bhattacharjee, Shreyash Das, Kapil Dhavale, Sahil Sawant
FitBalance – AI-Powered Fitness App	Dr. Naveeta Kant	Divya Bhatt, Arnav Malpathak, Mahee Prajapat, Khushi Sharma
MediVault: Intelligent Healthcare Documentation System	Mr. Abhijit Shete	Sujay Thasale, Swed Lengare, Hrishikesh Patil, Sidhik Thorat
Raksha: Smart Protection Device	Mrs. Amrita Jhaveri	Ritali Jadhav, Srushti Pawar, Purvi Prasad, Joanna Sanju
SafeFrame: AI Web Vulnerability Scanner	Dr. Gauri Sahoo	Mansi Mohite, Farhan Ansari, Rohan Panjwani, Harsh Kapse

ACADEMIC EXCELLENCE

Final Year Projects

PROJECT TITLE	PROJECT GUIDE	GROUP MEMBERS
VoxConnect – Multi-Language Voice App	Dr. Asawari Dudwadkar	Vighnesh Padwal, Aditya Mohapatra, Shreya Sakpal
ML-Based Network Threat Detection System	Dr. Gauri Sahoo	Shikha Sharma, Sumedh Chandra, Dhruv Kulkarni, Sanchit Kulkarni, Sakshi Raorane
NexScan: OCR Automation Platform	Mrs. Sarika Kuhikar	Vineet Wagh, Srushti Chopade, Sneha Patil, Darshan Kakad
NoRo – AI Copilot for EMS Documentation	Mr. Yogesh Pandit	Rohan Khamitkar, Rujul Salunkhe, Avantika Sharma, Yash Khanavkar
HairCraft AI – Digital Transformation Platform	Mrs. Sarika Kuhikar	Ananya Dubey, Rutuja Sawant, Niharika Vaidya, Isha Gavkar
EEG-Based Disease Prediction using Self-Supervised Models	Dr. Jaymala Adsul	Shubham Bhandary, Jyotiraditya Bhogi, Abhijeet Pandey, Eshaan Kachru
SwasthyaSetu – Health Information Portal	Dr. Kavita Tewari	Arnav Nadkarni, Vishwajeet Panaskar, Vinayak Panchal, Tejas Patil, Kartik Babu

ACADEMIC EXCELLENCE

Final Year Projects

PROJECT TITLE	PROJECT GUIDE	GROUP MEMBERS
AI-Based Chest X-ray Disease Detection	Mrs. Amrita Jhaveri	Harsh Chaurasiya, Soham Handore, Harikrishna Gurrapu, Manasvi Bhalerao
Blockchain-Based Agricultural Supply Chain System	Mr. Abhijit Shete	Riddhi Buva, Sourabh Gupta, Sucheit Joshi, Prajwal Kudapane

ACADEMIC EXCELLENCE

Inspiring the Juniors

Final Year Project Session

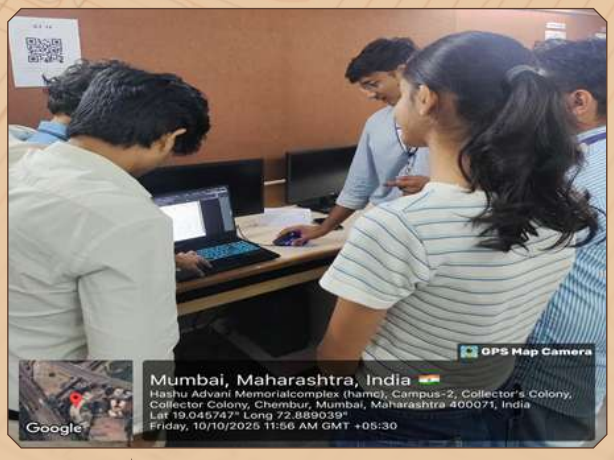
The Department of Electronics and Computer Science organized a Final Year Project Exhibition on 10th October 2025, providing a dynamic platform for final-year students to showcase their innovative projects. Spanning domains such as Artificial Intelligence, Web Development, Embedded Systems, and IoT, the exhibition reflected the diverse technical expertise and creativity of the students.

ACADEMIC EXCELLENCE

Inspiring the Juniors

The exhibition also encouraged knowledge sharing and collaboration, as students discussed methodologies, challenges, and implementation strategies behind their projects. This hands-on exposure bridged the gap between theoretical learning and real-world application, making the experience both engaging and educational.

Overall, the event served as a valuable learning platform, fostering innovation, critical thinking, and teamwork. It motivated students to explore new ideas and laid a strong foundation for their future academic and professional projects.



Final Year Project Session

ALUMNI VOICES

Industry, Research, Entrepreneurship

Malhar Chaudhari
Electronics Engineering,
Batch of 2015

Malhar Chaudhari is a senior technology leader who bridges business strategy and advanced engineering to help enterprises build and scale new products with confidence. As a Fractional CTO and Strategic Advisor, Malhar has worked with a diverse portfolio of startups across HealthTech, Prop Tech AI, Solar Deep Tech, and AI-driven EdTech products. He has helped founders and leadership teams progress from early product vision to scalable, enterprise-ready platforms.

This work is supported by deep engagement with the broader startup ecosystem, including close collaboration with VCs, product marketers, growth partners, and operators, enabling strong alignment between technology execution, go-to-market strategy, and investor expectations.

Backed by a strong research pedigree and industry recognition, Malhar brings a forward-looking, execution-focused approach that enables organizations to move quickly, de-risk critical decisions, and build scalable, future-ready products.

AI DEMOCRATIZED BUILDING, NOT JUDGEMENT.

What the AI Era Really Demands from Students Entering the Job Market

I spent the first five years of my career with my head buried in code. The ability to build, to take a problem and construct a working solution from scratch, felt like the most important skill in technology. It was the entry ticket. The thing that set engineers apart.

I no longer write code day-to-day. And yet, I have never been more effective.

That is not a boast. It is an invitation to think carefully about what the next decade of your career will actually demand of you. The rules have shifted, and the students who understand that shift early will have a meaningful head start.

The Question Has Changed

With AI-assisted tools, a determined engineer can now ship a functional prototype in a weekend. When building becomes this accessible, execution alone stops being a differentiator. The premium shifts to the thinking that happens before a single line of code is written: the judgement that sits upstream. I call this Technical Judgement.

Technical judgement is not about knowing the most advanced framework. It is about making architectural choices that hold up under pressure. Those choices need to serve three constraints, in sequence:

•Demonstrate Fast: Speed at this stage is a design choice, not an accident. It requires resisting the pull of elegance before you have earned it.

•Iterate without Rewrites: Structures that are painful to change will box you in at exactly the moment things start to work.

•Grow Cleanly: Security, compliance, and maintainability are not features you bolt on later. The engineers who design for growth from the start are the ones trusted with larger problems.

These are not three parallel concerns. They are a sequence. Each layer only becomes relevant once the one before it is handled. Knowing how to navigate that sequence, under real constraints with imperfect information, is what engineering judgement actually looks like in practice.

How to Develop It, Fast

Adam Grant, in Hidden Potential, draws a distinction I keep returning to. Clay is shaped by external pressure: it takes the form it is pushed into, then holds it. A sponge absorbs everything, retains it, and releases it when needed.

For most of engineering history, junior developers were shaped like clay. They made mistakes, senior engineers corrected them, and over years of supervised experience, they developed judgement. That model is breaking down. The pace of change has compressed what used to take years into months.

You now have access to a tireless sparring partner that will audit your code, challenge your architectural decisions, and explain the tradeoffs you did not know you were making. It will only make you better if you approach it like a sponge: absorbing every critique, sitting with every tradeoff, and refusing to move on before you genuinely understand.

Here is the practical shift I would encourage. Most student projects run one cycle: build it, submit it, move on. Your projects need to run in iterative modes. After each version, do not ask whether it works. Use an LLM as a rigorous judge: ask what a senior engineer would critique about your architecture, identify the tradeoffs you made, and surface the ones you missed. Work through each question carefully, understanding not just what the better answer is, but why.

What you are building, deliberately, is the feedback loop that senior engineers once provided: Build. Observe. Analyse. Decide. Iterate. That loop once took three to four years of supervised experience. With the tools available today, you can run it in months. But only if you resist the urge to skip ahead.

The students who will thrive are not those who build the fastest. They are the ones who learn the fastest, and who have the patience to turn that learning into durable judgement.

A Final Word

When I look back, I am grateful for those early years. Learning to build gave me the instincts to evaluate what I can no longer write myself.

But if I were starting today, I would treat every project as a deliberate practice ground for engineering judgement. I would use AI not only to write code faster, but to pressure-test my thinking at every step.

The opportunity in front of you is to become a more precise thinker about how things are built. In a world where anyone can build, that precision is the edge that compounds.

- Malhar Chaudhari

NAVIGATING THE FUTURE: MY JOURNEY IN VLSI DESIGN AT MICRON TECHNOLOGY

Madhura Shetti
Layout Designer,
Micron Technology

The rapid evolution of semiconductor technology and Artificial Intelligence (AI) has created exciting opportunities for engineers around the world. As industries increasingly depend on faster computation, efficient data storage, and intelligent systems, the demand for skilled professionals in Very Large-Scale Integration (VLSI) and AI continues to grow. My journey from studying VLSI during my academic years to working as a Layout Designer at Micron Technology has given me valuable insights into how these fields are shaping the future of technology. During my undergraduate studies in electronics engineering, I was introduced to the fundamentals of semiconductor devices, digital electronics, and integrated circuit design. Courses such as CMOS design, VLSI design, and semiconductor physics helped me understand how complex electronic systems are built at the transistor level. At that stage, VLSI seemed like a highly technical and challenging field, but it also fascinated me because it lies at the heart of every modern electronic device - from smartphones and laptops to data centers and AI hardware.

As part of my coursework, I worked on projects involving digital circuit design, simulation, and layout tools. Learning industry tools for schematic design, layout creation, and verification helped bridge the gap between theoretical knowledge and practical application. Concepts such as Design Rule Checking (DRC), Layout Versus Schematic (LVS), and parasitic extraction became central to understanding how chip designs move from concept to fabrication. After completing my studies, I began my journey at Micron Technology as a Layout Designer. Micron is a global leader in memory and storage solutions, and working in this environment has been both challenging and rewarding. My role primarily involves creating and optimizing the physical layout of integrated circuits. The layout stage is critical because it translates circuit schematics into geometric patterns that will eventually be fabricated on silicon wafers.

In my daily work, I focus on designing layouts that meet strict design rules while optimizing performance, area, and reliability. This requires careful attention to detail and a strong understanding of semiconductor manufacturing constraints. Collaboration is also an essential part of the job, as layout engineers work closely with circuit designers, verification teams, and process engineers to ensure the final design functions correctly.

The semiconductor industry is currently undergoing significant transformation. The rise of AI, machine learning, and high-performance computing has dramatically increased the demand for advanced memory technologies and specialized hardware accelerators. Companies are developing new architectures and fabrication techniques to meet these needs. In this context, VLSI engineers play a crucial role in enabling innovations that power AI applications, cloud computing, and next-generation devices. For students aspiring to build careers in VLSI or AI-related hardware design, several key skills are essential. First, a strong foundation in electronics fundamentals—such as digital design, analog circuits, and semiconductor physics—is critical. Second, familiarity with industry- standard Electronic Design Automation (EDA) tools is highly valuable. Skills in programming languages like Python or scripting languages can also be beneficial for automation and design optimization. Equally important are problem-solving abilities, attention to detail, and the willingness to continuously learn. The semiconductor industry evolves rapidly, and engineers must constantly adapt to new technologies, tools, and design methodologies. Teamwork and communication skills are also vital because chip development involves collaboration across multiple teams and disciplines.

However, working in this field also presents real-world challenges. Design complexity continues to increase as technology nodes shrink and chips integrate billions of transistors. Engineers must balance performance, power consumption, and manufacturability while meeting tight project deadlines. This environment requires persistence, analytical thinking, and a strong commitment to quality.

Reflecting on my journey, one of the most important lessons I have learned is that curiosity and continuous learning are key to growth. The transition from academic learning to industry work can be demanding, but it also provides opportunities to apply theoretical knowledge to real-world problems. Every project offers new challenges and insights that help build both technical expertise and professional confidence.

In conclusion, the fields of VLSI and AI offer immense potential for innovation and career growth. As technology continues to advance, engineers will play a crucial role in shaping the future of computing and intelligent systems. For students preparing to enter these domains, developing strong fundamentals, practical skills, and a passion for learning will open the door to exciting opportunities in the semiconductor industry.

- Madhura Vishvanath Shetti

Aditya is a PhD student in Computer Science and Engineering at UC Santa Cruz, specializing in hardware security, device modeling, and reliability. His research experience includes thesis work on BTI modeling using TCAD and SPICE to analyze device aging, timing degradation, and security vulnerabilities.

He used AI RAG workflows, enabling secure document access and improved engineering productivity. He is skilled in digital design, PCB Design, FPGA flows, and EDA tools. He is currently working on a physics-based simulation of device degradation in advanced process nodes like FinFETs and GAAFETs.

Aditya Ashish Bedekar
Electronics Engineering
Batch of 2023

While AI is not my primary focus, I do use LLMs and have done coursework in AI. I think writing optimal prompts and developing a good understanding of the computing infrastructure needed for AI is really relevant from a hardware perspective. In particular, the challenges with SRAMs and DRAMs, and in-memory computing. There are several eminent startups that solve hardware challenges for the AI industry, which underscores the importance of hardware engineering in the AI era. Machine learning for EDA (Electronic Design Automation) is another exciting field that integrates AI and VLSI.

Starting off with the basics, it is important to develop strong fundamentals and gain hands-on knowledge of open-source PDKs like Sky130 or ASAP7. After mastering these, I would also strongly recommend using open-source EDA tools before moving on to commercial alternatives, as this provides a well-rounded understanding of how EDA tools differ. Shifting focus to FPGAs, I believe there are several low-cost Indian-made options available now, based on what I discovered on X (formerly Twitter). FPGAs are a good medium to experiment and work on projects without incurring massive costs, and are widely used in datacenters and even space applications.

As my work in this domain now emphasizes hardware security and reliability, I have found it necessary to explore further into semiconductor physics. Branching into material sciences as a novel field for semiconductor materials is an emerging area worth exploring, as the industry searches for materials beyond Silicon. GaN and SiC, particularly, are being used for high-power and high-voltage applications.

I started my Masters with coursework in Computer Architecture and Data Structures. While working on a Cache Optimization project, I developed a renewed appreciation for hardware engineering. This led me to do coursework in VLSI design, ASIC and FPGA Design. I started working with my PhD Advisor's lab over the summer, on a project which I got to present at a conference in San Francisco. These experiences motivated me to pursue further research by doing a PhD, focusing on hardware security and reliability. I learned the hard way that applying concepts in projects helped me grasp them more effectively. For example, during an interview, I was asked about the mathematics underlying a project that I had worked on. Although I was able to explain it, my explanation was not convincing enough. Later, I discovered the ELI5 framework, short for 'explain it to me like I'm 5'. This approach is a good test of understanding, as one can explain a concept without jargon, only when they truly understand it.

I think LLMs are really helpful for the higher layers of abstractions in software engineering. As we move to the lower levels of abstraction, it becomes increasingly complex as data is often sparsely available and licensed. I am currently working on a project that would roughly fall in the Devices - Physics Category, and I had to undertake several courses to understand the foundational theory, as I was able to use the tools but lacked the necessary knowledge to progress further. This leads me to think that skills in the lower abstraction, particularly in hardware, along with the software engineering and AI abstraction, would make an excellent skill combination in the coming years.

– *Aditya Ashish Bedekar*

PLACEMENTS

Kartik Babu
CTC : 8.5 LPA

Srushti Pawar
CTC : 4 LPA

Harsh Kapse
CTC : 4.25 LPA

Mansi Mohite
CTC : 4.25 LPA

PLACEMENTS

Vinayak Panchal
CTC : 6 LPA

Tejas Patil
CTC : 4.25 LPA

Sneha Patil
CTC : 4.25 LPA

Niharika Vaidya
CTC : 4.25 LPA

PLACEMENTS

Ritali Jadhav
CTC : 6.48 LPA

Sakshi Raorane
CTC : 6.48 LPA

Vishwajeet Panaskar
CTC : 6.48 LPA

Rohan Panjwani
CTC : 10 LPA

PLACEMENTS

Shreyash Das
CTC: 16k stipend

Soham Handore
CTC: 16k stipend

STUDENT DEVELOPMENT

Workshops

DevOps Fundamentals Bootcamp

The Department of Electronics and Computer Science, in collaboration with Tinkerer's Lab ECS, conducted a five-day DevOps Fundamentals Bootcamp from 21st to 25th July 2025. Led by Mr. Malhar Chaudhari, the program introduced students to modern DevOps practices and tools including Git, Docker, Terraform, and Jenkins.

Workshop Photos

With a strong hands-on approach, students explored CI/CD pipelines, cloud simulation, and automation through real-world projects. The bootcamp not only strengthened technical skills but also encouraged teamwork and industry-oriented thinking, preparing students for the evolving demands of software engineering.

STUDENT DEVELOPMENT

Workshops

Fusion 2.0 – Tech Bootcamp

The Department of Electronics and Computer Science, in collaboration with Tinkerer's Lab ECS and the Institution's Innovation Council (IIC), organized Fusion 2.0 – Tech Bootcamp from 21st to 25th July 2025. The program offered a dynamic platform for students to explore diverse domains including IoT, Web Development, Version Control, and Digital Electronics.

Workshop Photos

Through hands-on sessions and project-based learning, participants worked with microcontrollers, cloud platforms, and modern development tools. The bootcamp fostered creativity, collaboration, and practical problem-solving, equipping students with essential technical skills and a strong foundation for real-world applications.

STUDENT DEVELOPMENT

Seminars

Business Model Canvas Session

The Department of Electronics and Computer Science, under the guidance of IQAC and IIC, conducted a Business Model Canvas Session on 22nd August 2025. The session was led by Mr. Monu Shetty, who introduced students to the fundamentals of entrepreneurship and business design.

Session's Photos

Through an interactive and hands-on approach, students explored the nine building blocks of the Business Model Canvas and worked in groups to develop and present their own business ideas. The session encouraged innovation, strategic thinking, and practical understanding of how ideas can be transformed into structured business models.

STUDENT DEVELOPMENT

Seminars

Innovations in Semiconductor Fabrication Technologies for High Speed Applications in VLSI

The Department of Electronics and Computer Science, in collaboration with IIC and under the guidance of IQAC, organized an Industry–Institute Interaction session on 30th October 2025 on the theme “Innovations in Semiconductor Fabrication Technologies for High-Speed Applications in VLSI.”

Session’s Photos

The session was delivered by Ms. Meri Helle, a semiconductor entrepreneur from Finland, who shared insights into modern fabrication processes, industry trends, and high-speed VLSI applications. Students gained a deeper understanding of semiconductor technologies and their real-world significance.

STUDENT DEVELOPMENT

Sessions Organized

Innovation to Impact: Patents and Startup Scaling Strategies

The Department of Electronics and Computer Science, under the guidance of IQAC and IIC, conducted a session on “Innovation to Impact: Patents and Startup Scaling Strategies” on 22nd September 2025. The session was led by Mr. Harish Yadav and Mr. Nilesh Shinde, who shared valuable insights on entrepreneurship, intellectual property, and startup growth.

Session’s Photos

Through an engaging and interactive approach, students explored the importance of patents, innovation protection, and strategies for scaling ideas into successful ventures. The session fostered entrepreneurial thinking and provided a practical understanding of how innovation can be transformed into real-world impact.

STUDENT ACTIVITIES

Placement Activities

- A session on Finishing School (Interview Preparation) was conducted by Mr. V. M. Joshi for D16 (2022–2026) on 24 July 2025 from 9:15 AM to 12:00 PM (Offline).
- A session on Finishing School (Data Structures – 1) was conducted by Mrs. Sangita Oswal for D16 (2022–2026) on 29 July 2025 from 9:30 AM to 11:30 AM (Offline).
- A session on Finishing School (Database Management System) was conducted by Mr. Abhijit Shete for D16 (2022–2026) on 30 July 2025 from 2:00 PM to 3:30 PM (Offline).
- A session on Finishing School (Object Oriented Programming) was conducted by Mrs. Indira Bhattacharya for D16 (2022–2026) on 31 July 2025 from 2:00 PM to 3:30 PM (Offline).
- A session on “Evolution of Engineering as the Backbone of Modern Society” was conducted by Dr. Padmanabhan Mahadevan for D11 (2023–2027) on 31 July 2025 from 1:30 PM to 2:30 PM (Offline).
- A session on Finishing School (Computer Communication Networks) was conducted by Dr. Mrs. T. Rajani Mangala for D16 (2022–2026) on 1 August 2025 from 2:00 PM to 3:30 PM (Offline).

STUDENT ACTIVITIES

Placement Activities

- A session on Finishing School (Alumni Interaction) was conducted by Mr. Harish Yadav, Ms. Akshara Viju, and Mr. Rahul Nair for D16 (2022–2026) on 1 August 2025 from 6:00 PM to 7:30 PM (Online).
- A session on Finishing School (Data Structures – 2) was conducted by Mrs. Kajal Joseph for D16 (2022–2026) on 2 August 2025 from 10:00 AM to 11:30 AM (Offline).
- A session on Finishing School (Resume Building and Personality Development) was conducted by Mrs. Urvashi Chawla for D16 (2022–2026) on 2 August 2025 from 12:00 PM to 1:30 PM (Offline).
- A session on Campus to Corporate (C2C) Program was conducted by Dr. Dashrath Mane for D6 (2024–2028) on 19 September 2025 from 10:30 AM to 11:30 AM (Offline).
- A session on Campus to Corporate (C2C) Program was conducted by Dr. Dashrath Mane for D11 (2023–2027) on 1 October 2025 from 11:30 AM to 12:30 PM (Offline).
- A session on Road to Success was conducted by Mrs. Sarika Kuhikar for D11 (2023–2027) on 10 October 2025 from 1:30 PM to 2:30 PM (Offline).
- A session on Competitive Programming was conducted by Mrs. Amrita Jhaveri for D6 (2024–2028) on 29 October 2025 from 11:30 AM to 12:30 PM (Offline).

STUDENT ACTIVITIES

Industrial Visits

CCTV Police Command Control Room

The Department of Electronics and Computer Science, under the guidance of IQAC and IIC, organized an Industrial Visit to the **CCTV Command and Control Facility, Belapur (Navi Mumbai)** on 4th October 2025.

Students gained valuable insights into the functioning of a large-scale city surveillance system, including network management, data storage, and real-time monitoring through video analytics.

CCTV Command and Control Facility, Belapur (Navi Mumbai)

The visit highlighted how technology is leveraged for public safety, emergency response, and smart city infrastructure.

This hands-on exposure bridged the gap between theoretical learning and real-world applications, enhancing students' understanding of modern surveillance and control systems.

STUDENT ACTIVITIES

Skill Enhancement Lecture

Design and Analysis Strategies of SRAM

The Department of Electronics and Computer Science, under the guidance of IQAC and IIC, conducted a Skill Enhancement Lecture on “Design and Analysis Strategies of SRAM” on 24th September 2025. The session was delivered by Mr. Jayachandar, Technical Director at M31 Tech Ltd., Bangalore.

Session’s Photos

The lecture provided insights into SRAM fundamentals, design techniques, and challenges in modern VLSI systems. Students gained exposure to real-world industry practices, including performance optimization, power efficiency, and advanced semiconductor design concepts.

INTERNSHIPS

NAME	COMPANY	DOMAIN
Rujul Salunkhe	Electrolyte Solutions	AI / App Development
Atharva Phadtare	Electrolyte Solutions	AI / App Development
Rohan Khamitkar	Electrolyte Solutions	AI / App Development
Harsh Kapse	Electrolyte Solutions	Web Development
Swed Lengre	Electrolyte Solutions	Web Development
Rohan Panjwani	Electrolyte Solutions	AI / App Development
Mansi Mohite	Electrolyte Solutions	Web Development
Vineet Wagh	Zymo	Web Development
Sakshi Raorane	Zymo	Backend Development
Yash Chillar	Finlatics	Machine Learning
Aayan Shaikh	Finlatics	Machine Learning

NAME	COMPANY	DOMAIN/PROJECT
Aditya Mohapatra	Plasmid	CyberSecurity
Saish Mhaskar	Kshitiksha Foundation	Content Writing, Marketing
Sanchit Kulkarni	Tech Sierra	TSP Optimization using AI
Nilesh Joshi	Zencommerce India Pvt. Ltd.	Web Development
Purvi Prasad	BARC	Large Language Model for Formal Text
Parnika Uparkar	ABM Knowledgeware Ltd.	Cybersecurity
Ritika Zare	Hydralite Nature Power Pvt. Ltd.	Dehumidifier System
Venkat Phaneendra Nittala	Maxval Technologies Pvt. Ltd.	Full Stack Development
Arnav Malpathak	AlongX Software	SaaS-based Project
Sahil Santosh Sawant	IISc Bangalore	Water Level Control System
Meenaakshi	TI Clean Mobility	3-Wheeler EV Overview
Shreyash Das	Aditya Birla Group	Network Pen Testing & Defense Tools

NAME	COMPANY	DOMAIN/PROJECT
Kanishk Sharma	Ernst & Young (EY)	SAP Digital Strategy
Hrishikesh Patil	Neosoft Pvt. Ltd.	AI/ML Research
Kapil Pramod Dhavale	Sony Pictures Network India	Cloud Engineering & Data Analytics
Vishakha R. Jangale	InAmigos Foundation	Graphic Designing
Maitrey Khedekar	SmartDwell Technologies (Navy Blue Energy Labs)	Mobile App Development
Riddhi Buva	TATA Communications Ltd.	AI CoE
Vinayak Panchal	TATA Communications Ltd.	AI CoE
Srushti Chopade	TATA Communications Ltd.	AI CoE
Viighnesh Padwal	Mytek Innovations Pvt. Ltd.	Web Development

CREATIVITY IN THE SPOTLIGHT

Utkarsh Poojari
Class: D6

CREATIVITY IN THE SPOTLIGHT

Hiya Dharwani
Class: D6

CREATIVITY IN THE SPOTLIGHT

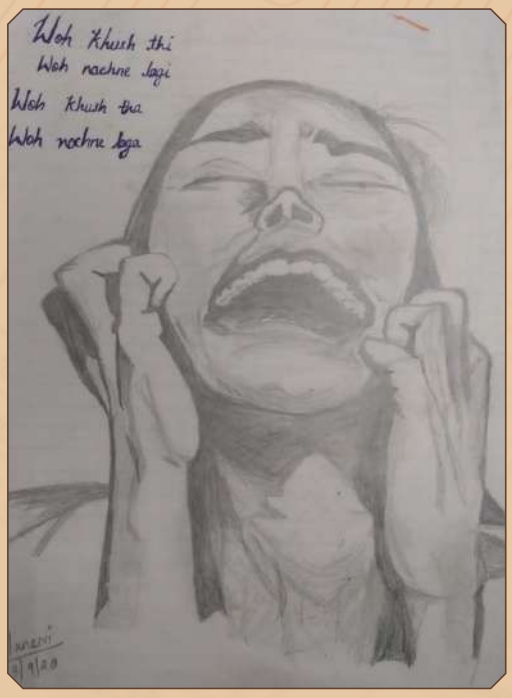
Shruti Thakare
Class: D11

CREATIVITY IN THE SPOTLIGHT

Vinay Yadav
Class: D11

CREATIVITY IN THE SPOTLIGHT

Reva Patne
Class: D11



Manasvi Bhalerao
Class: D16

CREATIVITY IN THE SPOTLIGHT

They speak of rainbows in the storm,
Of colors dancing while skies deform—
As if beauty must bloom through ache,
As if pain is art for beauty's sake.

But I have seen a rarer light,
Not painted bold against the night—
A trembling grin on tearstained skin,
Where sorrow folds its truth within.

No thunder claps, no colors fly;
Just silence held beneath the eye.
A smile worn like armor thin—
Not joy, but courage tucked within.

For rainbows dazzle from the skies,
But grief smiles low, where silence lies.
And in that hush, so soft, so brave—
The heart learns how the hurt behaves.

So if you ask what glory means,
Don't look just to the silver scenes—
But to the soul that dares to rise,
With tears still clinging to its eyes.

Hiya Dharwani
Class: D6

CREATIVITY IN THE SPOTLIGHT

I wonder what butterflies in stomach feels like (ᵔᵕᵔ✻)

I wonder what butterflies in stomach would look like, is it like the birds chirping on a bright shiny morning or is it like the ripples of river water streaming.

OR does it resembles to the beauty of cascade water flowing!

I wonder what butterflies in stomach would smell like, is it like the fragrance of mogra in the air or is it like the smell of petichor on a cozy sunday evening

OR does it romanticizes the art of pottery!

I wonder what butterflies in stomach would look like, is it like a glimpse of that one favourite person of yours or is it like that conversation with the stranger you once met

OR does it feel like that awkward smile, when you create a mess in the grocery store!

I wonder what butterflies in stomach feels like (ᵔᵕᵔ✻)

Srushti Chopade
Class: D16

CREATIVITY IN THE SPOTLIGHT

Being a hopless romantic lover
I will always,

In a group of people i will still choose you
In a room full of art i will still stare you
No matter what i will care for you
If its a curse, still i will draw you
I will crack the dummest jokes to make you smile
Seeing you happy will always be a goal for me
And having you beside me will be a dream for me

If thing comes about patients
Time will pass but i will be still waiting for you

Everything that i do will be less everytime,
As you had me a lot more than you think

How taylor describes it in her song
No one ever had me
"Had me" Not like you

And yess i had started listening to your favourite singers too.....

Kalpesh Rathod
Class: D16

CREATIVITY IN THE SPOTLIGHT

जेव्हा जुळून येईल भेट अपुली
आभाळ वाहून जातील गं

तुझात मी आणि माझाता तू
अशी साथ कायम राहील गं

डोळे इतके सुंदर तुझे
माझे जग अडकून राहील गं

कधी ना हटेल मन माझे
नुसता तुझात गुंतून राहील गं

भीती नेहमी ही मनात माझा
तुला दुसऱ्याची नजर लागून जाईन गं

वेडा झालोय जणू इतका
देवा समोर ही तुलाच मागू गं

Kalpesh Rathod
Class: D16

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Dr. Jayamala Adsul
Assistant Professor, Department of
Electronics and Computer Science

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Hiya Dharwani

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